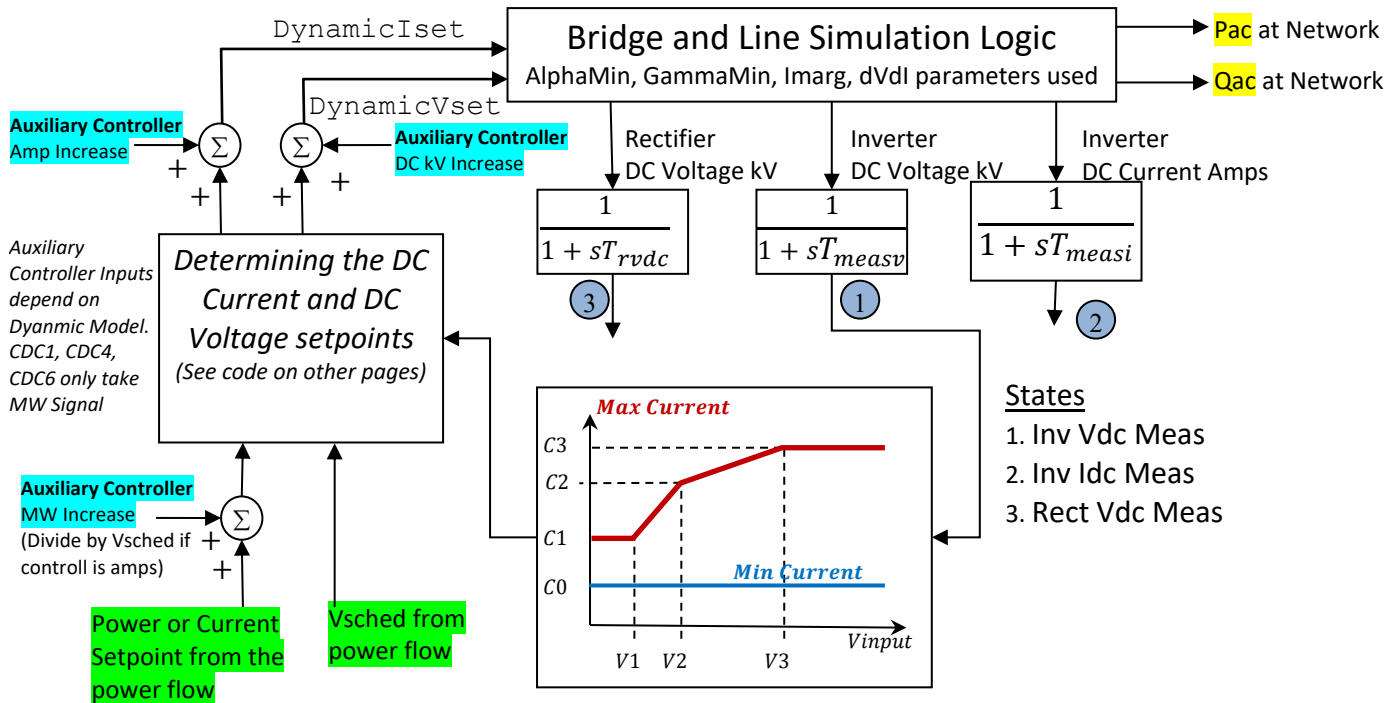


HVDC CDC1T, CDC6, CDC6T, and CDC4T

Two terminal D.C. Transmission



Bridge and Line Simulation Logic	AlphaMin, GammaMin (degrees) Imarg (part of CDC6 model, CDC6T and CDC4T use DELTI from the power flow record) dVdI (0.0 unless dynamic model includes parameter)
Measurement delays	Tmeasv, Tmeasi, Tvrdc (seconds)
Parameters for Voltage-Dependent Current Order Limiter (VDCOL)	C0, C1, C2, C3 (parameter in Amps) V1, V2, V3 (parameters in kV)
Block and Unblock rectifier without a time delay on under AC rectifier voltage	Vblock (per unit AC voltage for blocking) Vunbl (per unit AC voltage for unblocking)
How long a rectifier will remain blocked once decision is made to block it.	Tblock (seconds)
Bypass and Unbypass inverter without a time delay on under DC inverter voltage	Vbypas (per unit DC voltage for bypass)
How long an inverter will remain bypassed once a decision is made to bypass it	Tbypass (seconds)
Parameters use when restarting a DC line after performing a block or bypass action	Rsvolt (DC kV) Rscur (DC Amps) Vramp (pu/second) (per unit of DynamicVset) Cramp (pu/second) (per unit of DynamicIset)

The following features are not available for the CDC4T model

Block and unblock using an AC voltage <i>with a time delay in the decision</i>	Vdeblk (per unit AC voltage) Trblk (time delay for blocking) Treblk (time delay for unblocking use Vunbl)
Block Rectifier based on the inverter AC voltage including a communication time delay	Vinblk (per unit AC voltage) Tinblk (time delay for block decision in seconds) Tcomb (communication time delay)
Bypass and Unpass inverter	Vacbyp (per unit AC voltage) Tdebyp (time delay for bypass decision in seconds)

Model supported by PSSE

For a Detailed Treatment of how DC lines are implemented in the software see the help topic at https://www.powerworld.com/WebHelp/#TransientModels_HTML/Overview%20of%20HVDC%20Modelling.htm